

2005.11.17 08:59:00

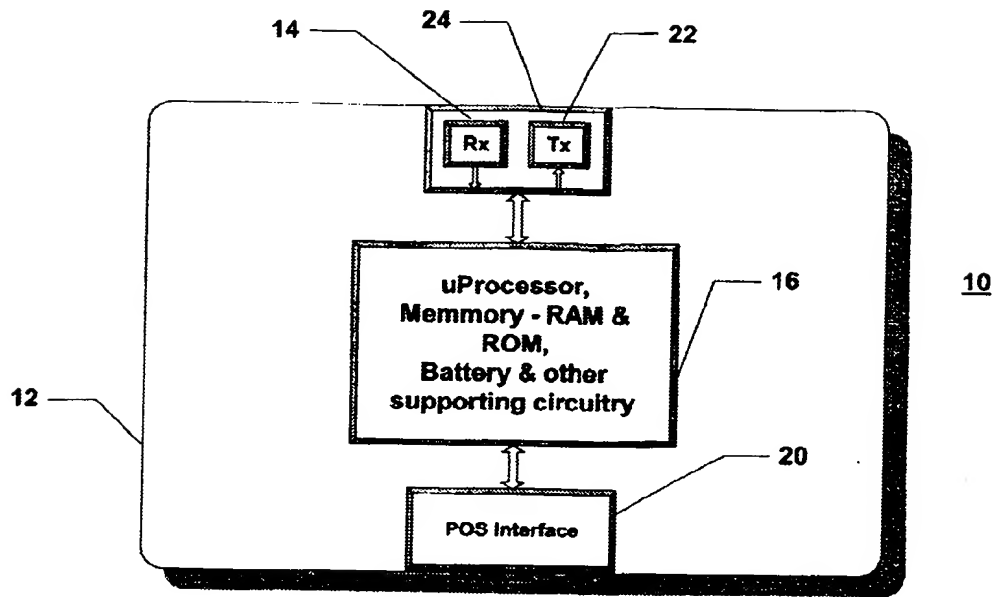


FIG. 1

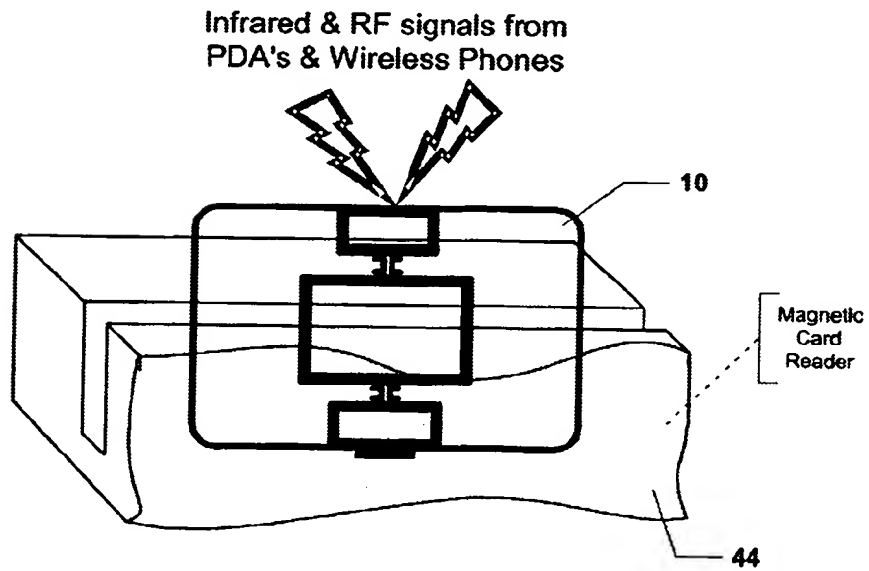


FIG. 2

The diagram illustrates the architecture of an Electronic Transaction Device (10). At the top, an external **Electronic Transaction Device** (60) contains an **Ir or RF Device** (68). This device communicates wirelessly (indicated by a lightning bolt) with the internal **Rx** (Receiver) and **Tx** (Transmitter) components (14 and 24) of the main device (10). The main device (10) also includes a **uProcessor, Memory - RAM & ROM, Battery & other supporting circuitry** (22) and a **POS Interface** (20). Bidirectional arrows indicate data flow between the communication components and the processor, and between the processor and the POS interface.

FIG. 3



FIG. 4

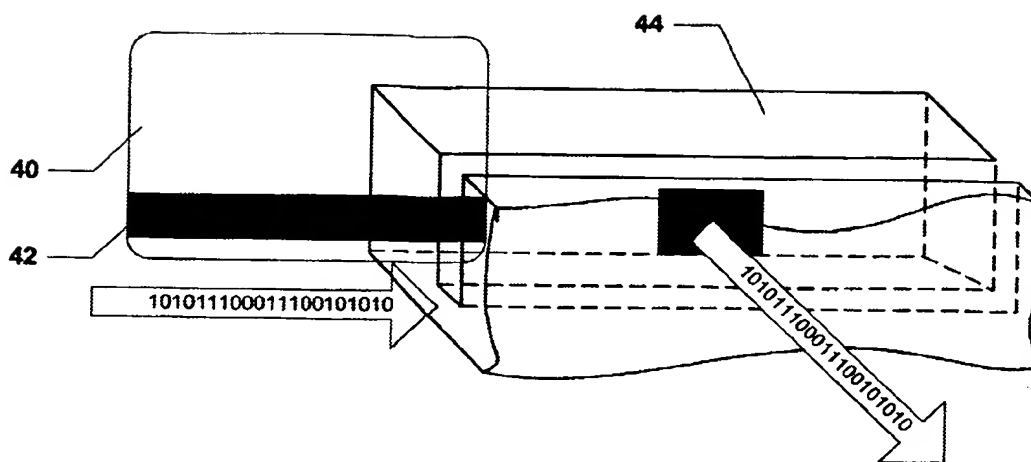


FIG. 5

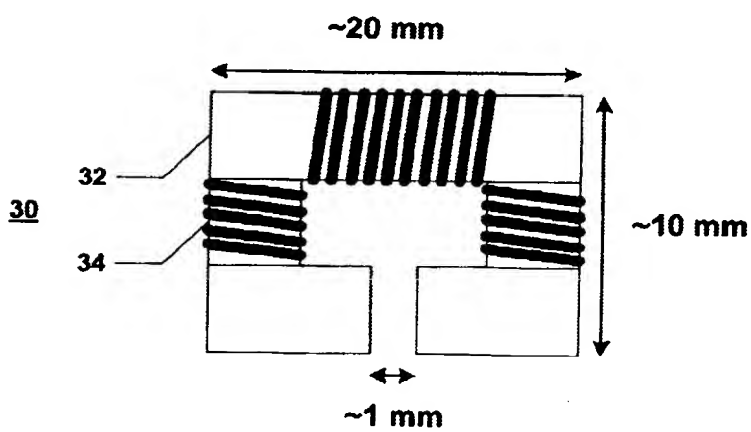


FIG. 6

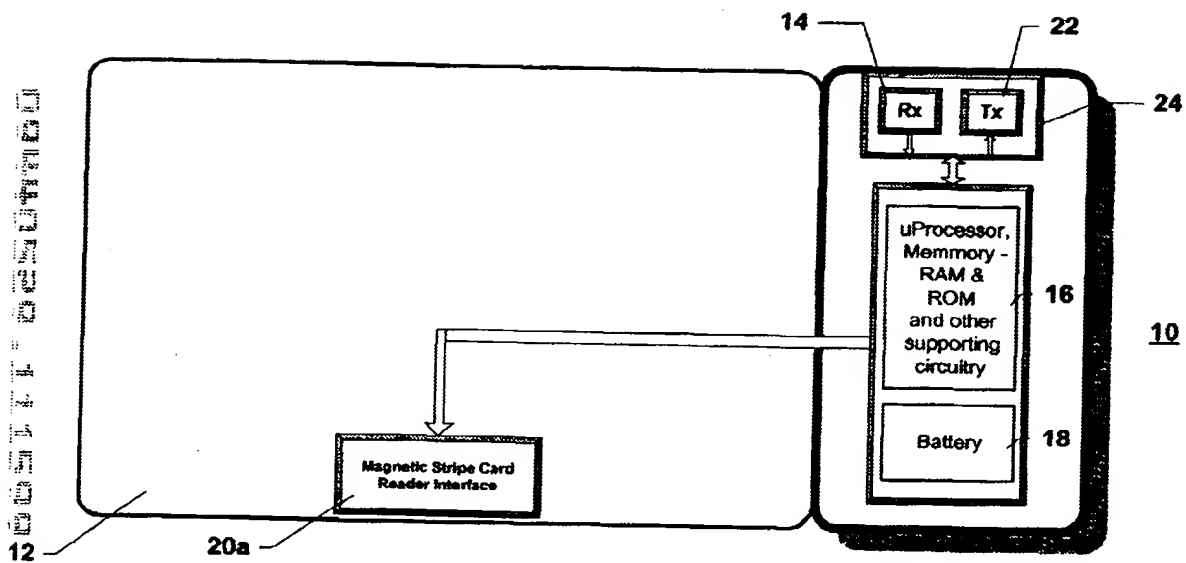
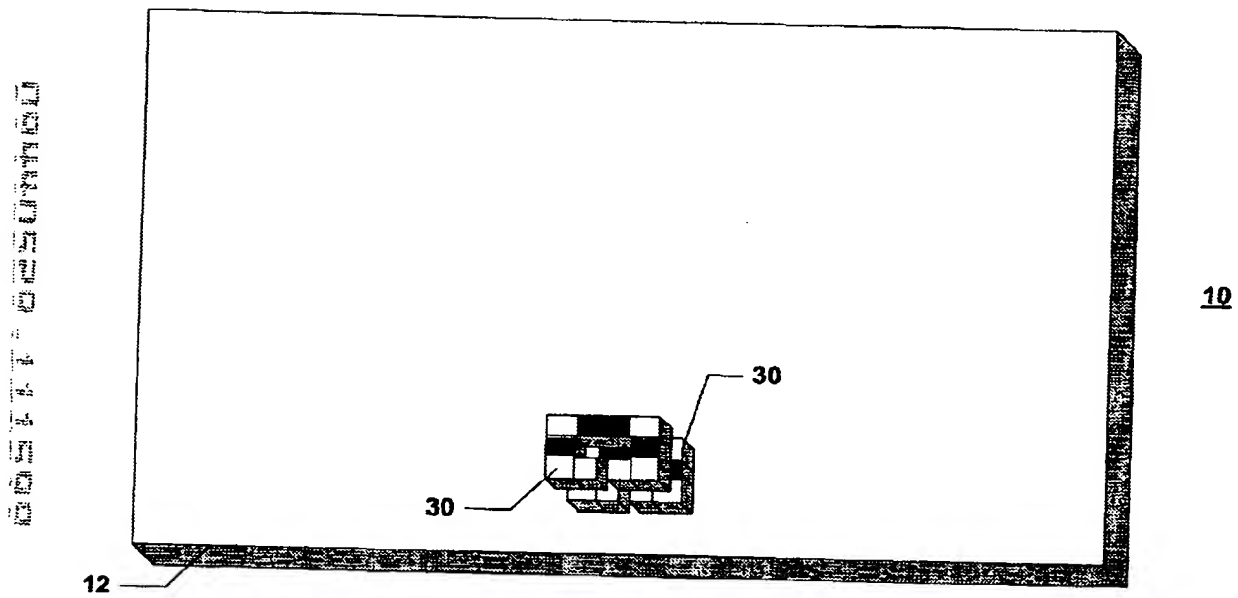


FIG. 7



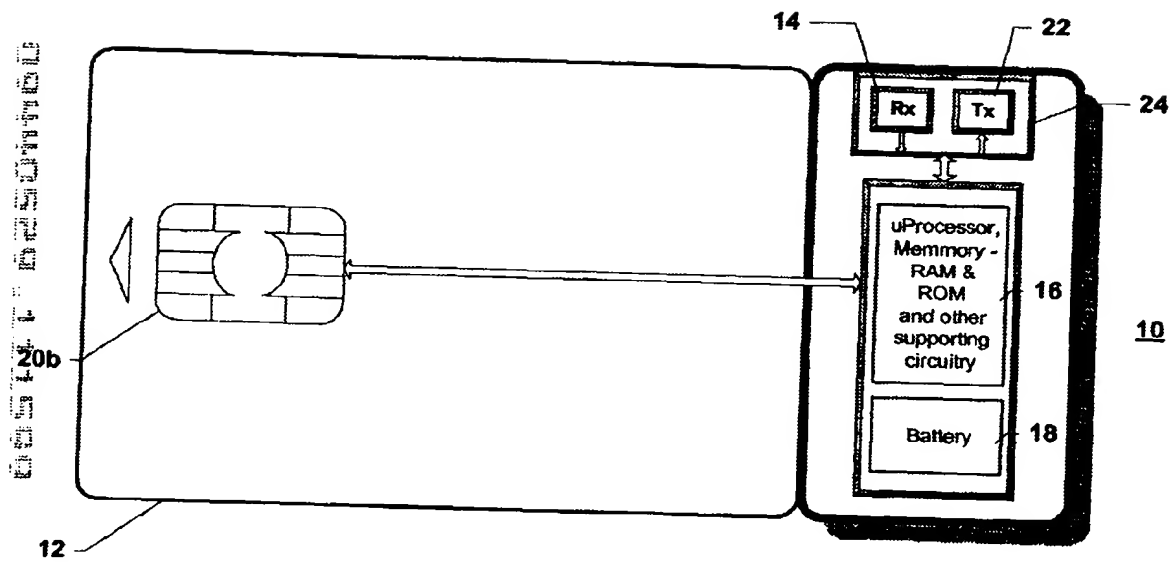


FIG. 9

Patented Dec 1, 1993

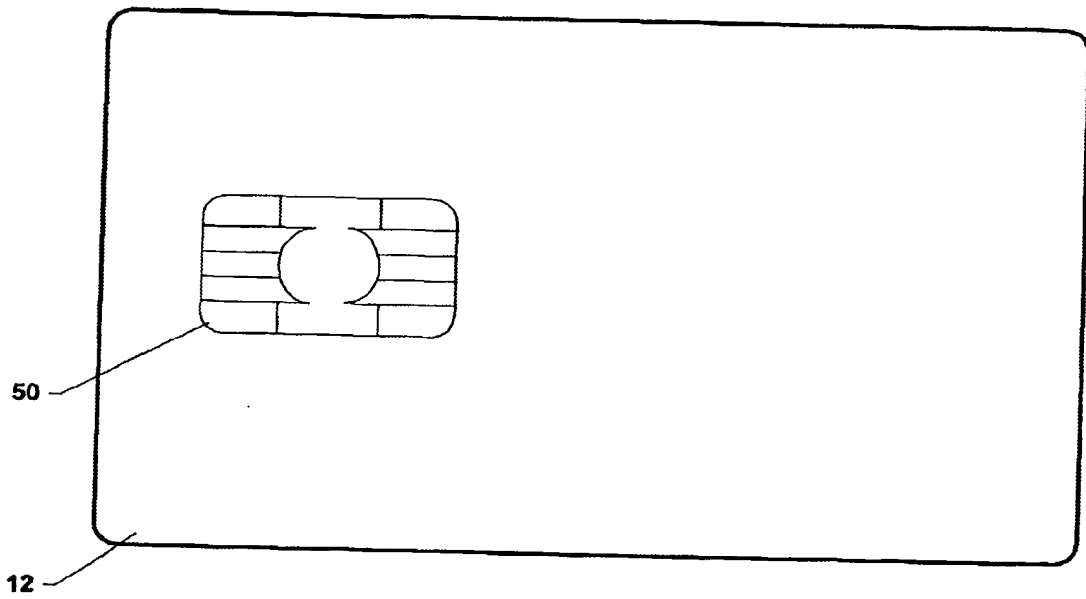


FIG. 10

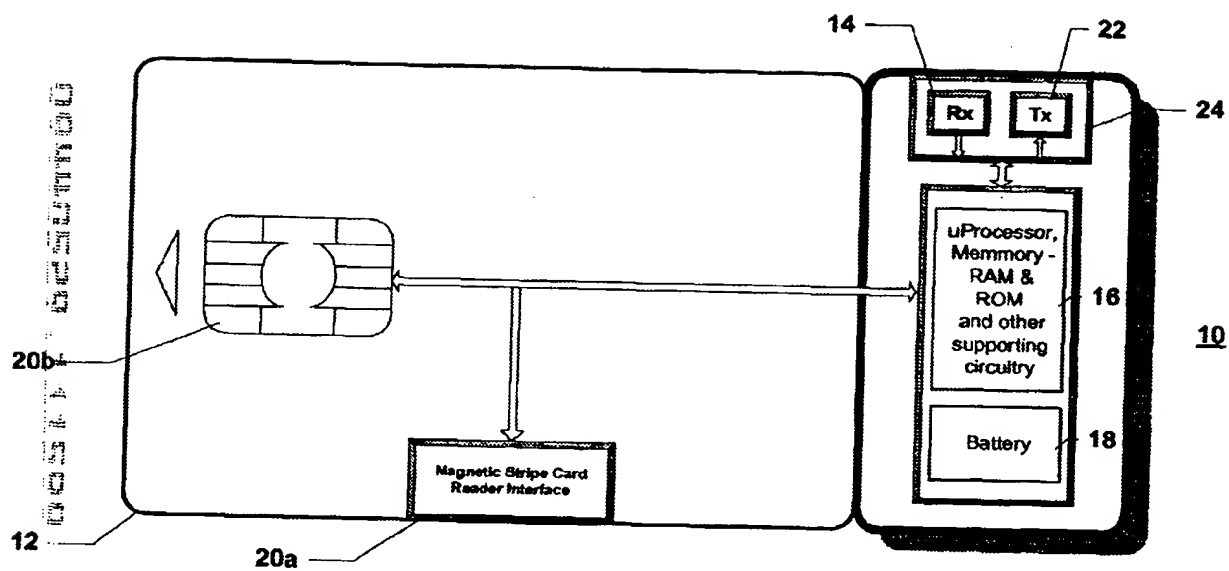


FIG. 11

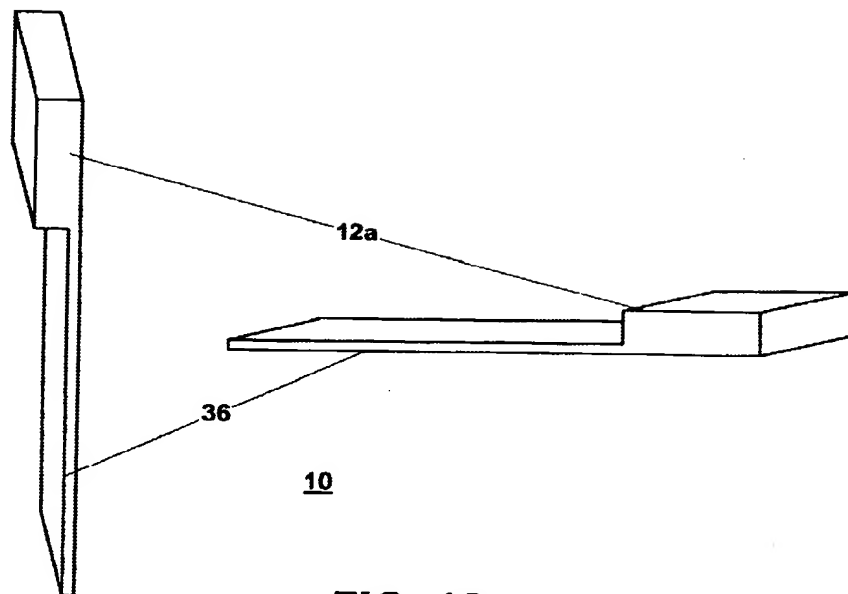


FIG. 12

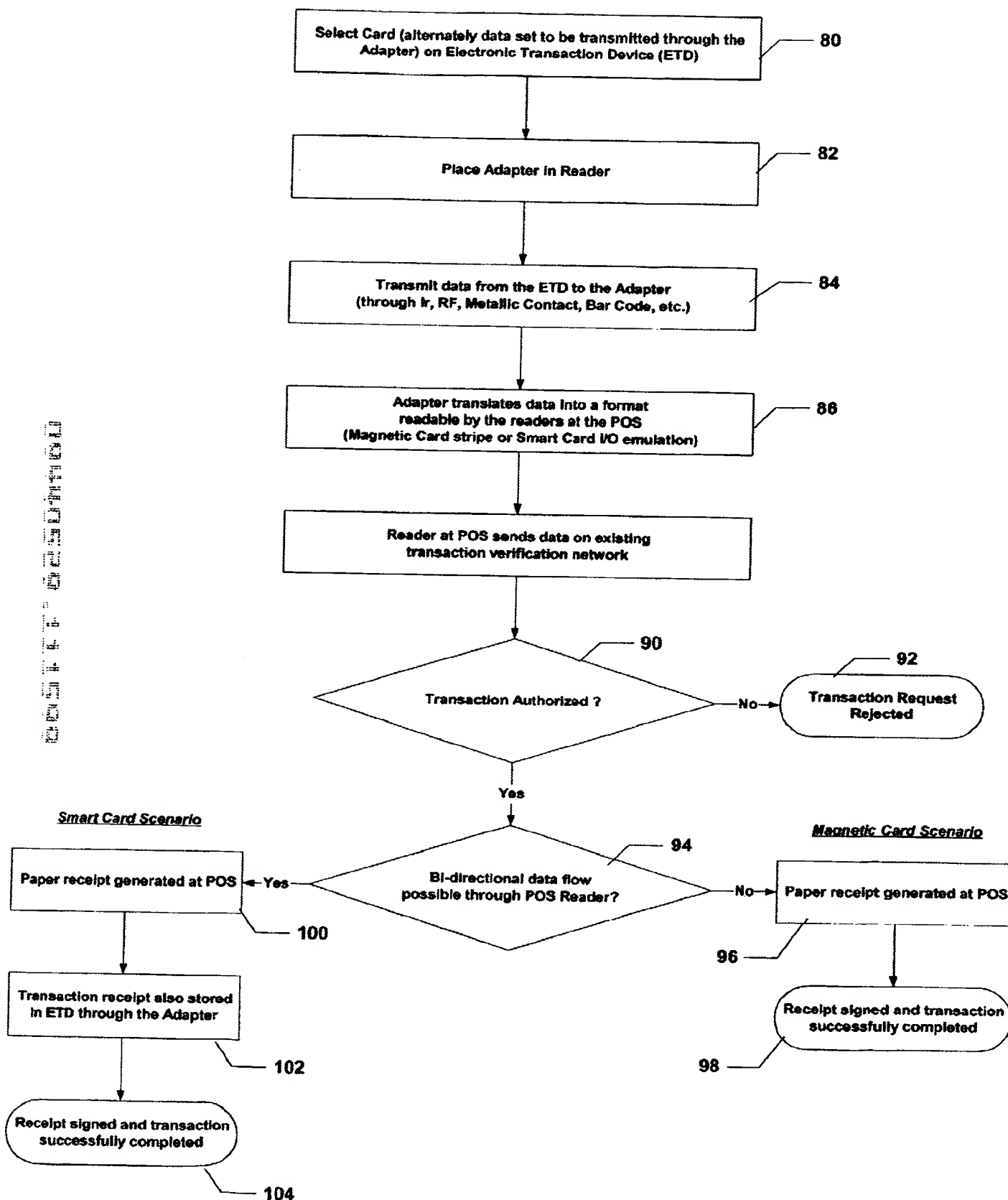


FIG. 13

FIG. 14

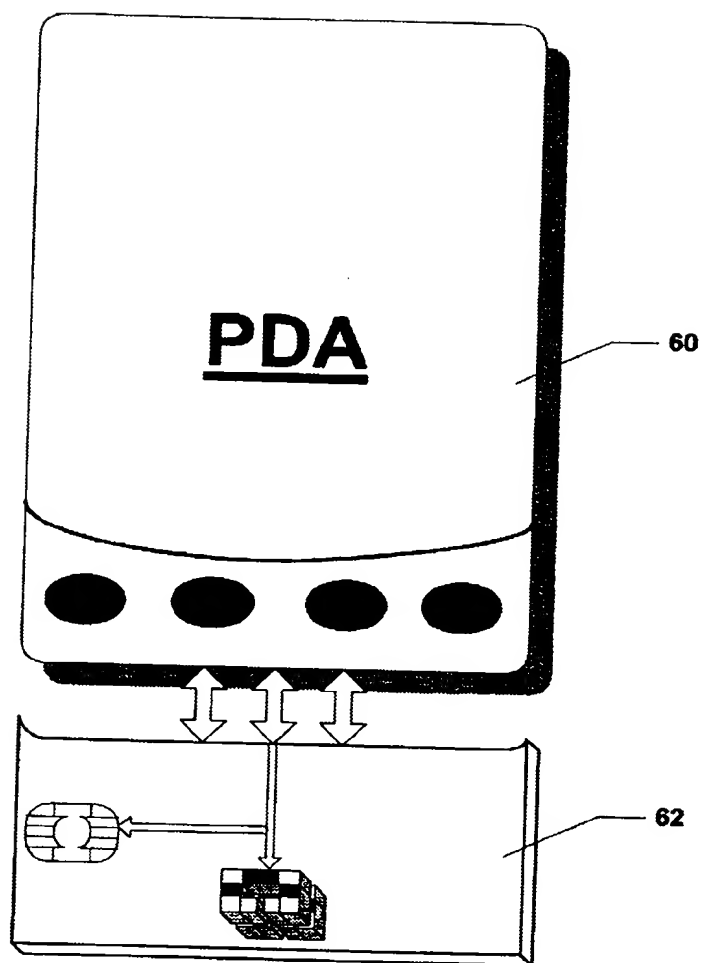


FIG. 14

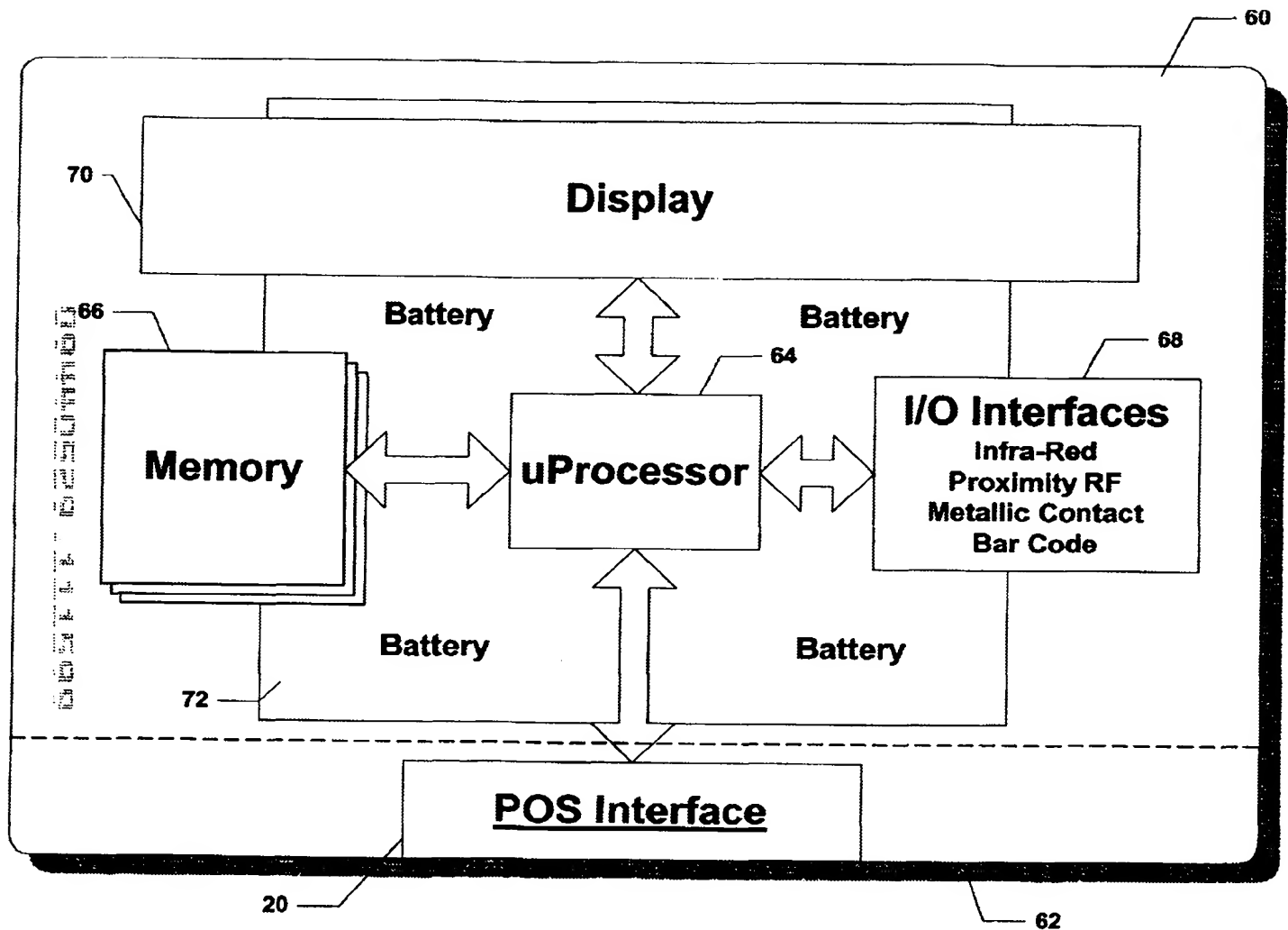


FIG. 15